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APPLICATION NO.		FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/047,992		01/17/2002	Hitoshi Ohashi	020052	5363
23850	7590	07/11/2005		EXAMINER	
ARMSTRO		RATZ, QUINTOS,	TALBOT, BRIAN K		
SUITE 1000		•	ART UNIT	PAPER NUMBER	
WASHINGTON, DC 20006				1762	
				DATE MAIL ED. 07/11/200	<i>E</i>

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)	
Office Action Summany	10/047,992	OHASHI ET AL.	
Office Action Summary	Examiner	Art Unit	
The MAU ING DATE of this communication and	Brian K. Talbot	1762	
The MAILING DATE of this communication app Period for Reply	•		
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days rill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONE	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on <u>election</u> This action is FINAL . 2b)⊠ This Since this application is in condition for allowart closed in accordance with the practice under Election.	action is non-final. nce except for formal matters, pro		
Disposition of Claims			
 4)	5 <u>,16 and 19-50</u> is/are withdrawn fi d.	rom consideration.	
Application Papers			
9) The specification is objected to by the Examine 10) The drawing(s) filed on 17 January 2002 is/are: Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	a)⊠ accepted or b)⊡ objected drawing(s) be held in abeyance. See ion is required if the drawing(s) is obj	e 37 CFR 1.85(a). jected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicati rity documents have been receive u (PCT Rule 17.2(a)).	on No ed in this National Stage	
Attachment(s)			
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 6/28/05. 	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:		

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1. The election of Group I, claims 1-20 and specie A, claims 1,4,7,8,11,14,17 and 18, is acknowledged. Non-elected claims 2,3,5,6,9,10,12,13,15,16 and 19-50 should be canceled in response to this Office Action.

Specification

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

No apparatus claims are present as well as the claims being prosecuted are directed toward jetting molten metal to form an electrical circuit.

Claim Rejections - 35 USC § 112

3. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 8 and 18 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

The claims are unclear as how the molten metal is applied to the insulator if the insulator is applied on the electrical circuit? Should this claim recite a method further comprising jetting a second molten metal layer on top of the insulator layer? Clarification is requested.

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4. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1,4,7,8,11,14,17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sterett et al. (5,746,844) in combination with Kudoh et al. (4,656,048).

Sterett et al. (5,746,844) teaches a method and apparatus for creating a three dimensional article using a layer-by-layer deposition of molten metal and annealing. The molten metal is applied by depositing the droplets in a predetermined pattern and rate (abstract).

Sterett et al. (5,746,844) fails to teach measuring and comparing data calculated by a monitoring device to control the deposited material.

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Kudoh et al. (4,656,048) a method of forming thick film circuit patterns with a sufficiently wide and uniform strip. The monitoring system measures and controls the distance of the nozzle form the substrate and compares that to a set value and performs and necessary changes to maintain the desired value (col. 2, lines 30-40, col. 3, line 55 – col. 4, line 35).

Therefore it would have been obvious for one skilled in the art at the time the invention was made to have modified Sterett et al. (5,746,844) deposition process by incorporating a measuring/control system as evidenced by Kudoh et al. (4,656,048) to produce the desired circuit pattern.

Claims 1,4,11 and 14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 in combination with Kudoh et al. (4,656,048).

Orme-Marmerelis et al. (6,520,402) teaches a high speed direct writing with metallic microspheres. Small droplets of molten metal are generated toward a substrate to form conductive traces (abstract).

JP 10-226,803 teaches a three dimensional body formed by various kids of materials.

Molten metal is spouted from a nozzle (10) to form droplets (20) which are applied to a substrate to form electric circuits (abstract).

Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 fail to teach measuring and comparing data calculated by a monitoring device to control the deposited material.

Features described concerning Kudoh et al. (4,656,048) above are incorporated here.

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Claims 7,8,17 and 18 are rejected under 35 U.S.C. 103(a) as being unpatentable over Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 in combination with Kudoh et al. (4,656,048) further in combination with Sterett et al. (5,746,844) or Pan (6,501,663).

Features described above concerning Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 in combination with Kudoh et al. are incorporated here.

Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 in combination with Kudoh et al. fail to specifically teach forming an insulating layer atop the molten layer.

Features described above concerning Sterett et al. (5,746,844) are incorporated here.

Pan (6,501,663) teaches a three dimensional interconnect whereby an interconnect is covered with an insulator layer to protect the interconnect (abstract and Figs 5-7).

Therefore it would have been obvious for one skilled in the art at the time the invention was made to have modified Orme-Marmerelis et al. (6,520,402) or JP 10-226,803 in combination with Kudoh et al. by incorporating an insulator layer atop the molten metal circuit layer as evidenced by either Sterett et al. (5,746,844) or Pan (6,501,663) with the expectation of achieving a multilayered structure or a protective layer for the circuitry.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian K. Talbot whose telephone number is (571) 272-1428. The examiner can normally be reached on Monday-Friday 6AM-3PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy H. Meeks can be reached on (571) 272-1423. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Brian K Talbot Primary Examiner Art Unit 1762

B-KTally 7/7/05

BKT